MAHARSHI DAYANAND UNIVERSITY, ROHTAK

(Established Under Haryana Act No. 25 of 1975)
("A+" Grade, NAAC Accredited)

J973

DMC No. 33891578
Roll No. 7518640
Regn. No. 2013761158

RESULT-cum-DETAILED MARKS CARD

BACHELOR OF TECHNOLOGY
(COMPUTER SCIENCE ENGG.) 1ST SEMESTER

EXAM - FEB 2022

Name :	UJJAWAL KUMAR JHA
Father's Name : Shri	RAMLAKHAN JHA
Mother's Name : Smt.	KIRAN JHA

DETAIL OF MARKS

Sr. No.	Subject Code	Subject Name	Marks obtained	Minimum Marks	Maximum Marks
1.	BSC-CH-101G	CHEMISTRY-I	034	030	075T
			016 050	040	025S 100
2.	BSC-CH-102G	CHEMISTRY LAB-I	022	010	025P
			015 037	020	025S 050
3.	BSC-MTH-103G	MATHEMATICS-I (CALCULUS AND LINEAR ALGEBRA)	048	030	075T
			017 065	040	025S 100
4.	ESC-CSE-101G	PROGRAMMING FOR PROBLEM SOLVING	030	030	075T
			016 046	040	025S 100
5.	ESC-CSE-103G	PROGRAMMING IN C LAB	016	010	025P
			018 034	020	025S 050
6.	ESC-ME-102G	WORKSHOP TECHNOLOGY	050	030	075T
			019 069	040	025S 100
7.	ESC-ME-103G	MANUFACTURING PRACTICE LAB	020	010	025P
			017 037	020	025S 050
8.	HSMC-ENG101G	ENGLISH	046	030	075T
			017 063	040	025S 100
TOTAL :			0401		0650

Result : PASSED AND HAS OBTAINED FOUR HUNDRED ONE MARKS
Dated : MAY 20, 2022
Place : Rohtak
Checked by 1. RB 2.
(Full Signature) (Clerk) (Assistant)
Date of issue 01/09/2023

CONTROLLER OF EXAMINATIONS

MAHARSHI DAYANAND UNIVERSITY, ROHTAK

(Established Under Haryana Act No. 25 of 1975)
("A+" Grade, NAAC Accredited)

J973

DMC No. 33819570
Roll No. 2005811
Regn. No. 2013761158

RESULT-cum-DETAILED MARKS CARD

BACHELOR OF TECHNOLOGY
(COMPUTER SCIENCE & ENGG.) 2ND SEMESTER

EXAM - JUL 2021

Name :	UJJAWAL KUMAR JHA
Father's Name : Shri	RAMLAKHAN JHA
Mother's Name : Smt.	KIRAN JHA

DETAIL OF MARKS

Sr. No.	Subject Code	Subject Name	Marks obtained	Minimum Marks	Maximum Marks
1.	BSC-MTH-104G	MATH-II (PROBABILITY AND STATISTICS)	047	030	075T
			010 057	040	025S 100
2.	BSC-PHY-103G	PHYSICS-I (SEMICONDUCTOR PHYSICS)	048	030	075T
			020 068	040	025S 100
3.	BSC-PHY-113G	SEMICONDUCTOR PHYSICS LAB	020	010	025P
			020 040	020	025S 050
4.	ESC-EE-101G	BASIC ELECTRICAL ENGG.	041	030	075T
			016 057	040	025S 100
5.	ESC-EE-102G	BASIC ELECTRICAL ENGG. LAB	011	010	025P
			016 027	020	025S 050
6.	ESC-ME-101G	ENGINEERING GRAPHICS & DESIGN	030	030	075P
			012 042	040	025S 100
7.	HSMC-ENG102G	LANGUAGE LAB	021	010	025P
			020 041	020	025S 050
TOTAL :			0332		0550

Result : PASSED AND HAS OBTAINED THREE HUNDRED THIRTY TWO MARKS
Dated : OCT 01, 2021
Place : Rohtak
Checked by 1. RB 2.
(Full Signature) (Clerk) (Assistant)
Date of issue 01/09/2023

CONTROLLER OF EXAMINATIONS

MAHARSHI DAYANAND UNIVERSITY, ROHTAK

(Established Under Haryana Act No. 25 of 1975)
("A+" Grade, NAAC Accredited)

J973

DMC No. 33893097
Roll No. 7025716
Regn. No. 2013761158

RESULT-cum-DETAILED MARKS CARD

BACHELOR OF TECHNOLOGY
(COMPUTER SCIENCE & ENGG.) 3RD SEMESTER

EXAM - FEB 2022

Name :	UJJAWAL KUMAR JHA
Father's Name : Shri	RAMLAKHAN JHA
Mother's Name : Smt.	KIRAN JHA

DETAIL OF MARKS

Sr. No.	Subject Code	Subject Name	Marks obtained	Minimum Marks	Maximum Marks
1.	BSC-MTH-203G	MATHEMATICS-III (MULTIVARIABLE CALC. AND DIFF. EQU.)	062 016 078	030 040	075T 025S 100
2.	HSMC-01G	ECONOMICS FOR ENGINEERS	042 020 062	030 040	075T 025S 100
3.	LC-CSE-209G	DATABASE MANAGEMENT SYSTEMS LAB	018 019 037	010 020	025P 025S 050
4.	LC-CSE-211G	DIGITAL ELECTRONICS LAB	018 020 038	010 020	025P 025S 050
5.	LC-CSE-213G	DATA STRUCTURES & ALGORITHMS LAB USING C	017 020 037	010 020	025P 025S 050
6.	PCC-CSE-201G	DATABASE MANAGEMENT SYSTEMS	052 019 071	030 040	075T 025S 100
7.	PCC-CSE-203G	DATA STRUCTURES & ALGORITHMS	032 021 053	030 040	075T 025S 100
8.	PCC-CSE-205G	DIGITAL ELECTRONICS	047 018 065	030 040	075T 025S 100
9.	PCC-CSE-207G	PYTHON PROGRAMMING	056 018 074	030 040	075T 025S 100
10	LC-CSE-215G	PYTHON PROGRAMMING LAB	017 018 035	010 020	025P 025S 050
TOTAL :			0550		0800

Result : PASSED AND HAS OBTAINED FIVE HUNDRED FIFTY MARKS
Dated : MAY 20, 2022
Place : Rohtak
Checked by 1. RB 2. Jey
(Full Signature) (Clerk) (Assistant)
Date of issue 01/09/2023

CONTROLLER OF EXAMINATIONS

MAHARSHI DAYANAND UNIVERSITY, ROHTAK

(Established Under Haryana Act No. 25 of 1975)
("A+" Grade, NAAC Accredited)

J973

DMC No. 34096128
Roll No. 4531384
Regn. No. 2013761158

RESULT-cum-DETAILED MARKS CARD

BACHELOR OF TECHNOLOGY
(COMPUTER SCIENCE & ENGG.) 4TH SEMESTER

EXAM - MAY 2023

Name :	UJJAWAL KUMAR JHA
Father's Name : Shri	RAMLAKHAN JHA
Mother's Name : Smt.	KIRAN JHA

DETAIL OF MARKS

Sr. No.	Subject Code	Subject Name	Marks obtained	Minimum Marks	Maximum Marks
1.	HSMC-02G	ORGANIZATIONAL BEHAVIOUR	035 014 049	030 040	075T 025S 100
2.	LC-CSE-212G	OPERATING SYSTEM LAB	022 015 037	010 020	025P 025S 050
3.	LC-CSE-214G	OBJECT ORIENTED PROGRAMMING LAB USING C++	016 017 033	010 020	025P 025S 050
4.	LC-CSE-216G	WEB TECHNOLOGIES LAB	017 015 032	010 020	025P 025S 050
5.	MC-106G	ENVIRONMENTAL SCIENCE	038 020 058	030 040	075T 025S 100
6.	PCC-CSE-202G	DISCRETE MATHEMATICS	035 014 049	030 040	075T 025S 100
7.	PCC-CSE-204G	COMPUTER ORGANIZATION & ARCHITECTURE	048 014 062	030 040	075T 025S 100
8.	PCC-CSE-206G	OPERATING SYSTEM	044 014 058	030 040	075T 025S 100
9.	PCC-CSE-208G	OBJECT ORIENTED PROGRAMMING	030 016 046	030 040	075T 025S 100
10	PCC-CSE-210G	WEB TECHNOLOGIES	030 015 045	030 040	075T 025S 100
TOTAL :			411		750

Result : PASSED AND HAS OBTAINED FOUR HUNDRED ELEVEN MARKS
Dated : JUL 17, 2023
Place : Rohtak
Checked by 1. _____ 2. _____
(Full Signature) (Clerk) (Assistant)
Date of issue 01/09/2023

CONTROLLER OF EXAMINATIONS

MAHARSHI DAYANAND UNIVERSITY, ROHTAK

(Established Under Haryana Act No. 25 of 1975)
("A+" Grade, NAAC Accredited)

J973

DMC No. 34159462
Roll No. 5549292
Regn. No. 2013761158

RESULT-cum-DETAILED MARKS CARD

BACHELOR OF TECHNOLOGY
(COMPUTER SCIENCE & ENGG.) 5TH SEMESTER

EXAM - DEC 2023

Name :	UJJAWAL KUMAR JHA
Father's Name : Shri	RAMLAKHAN JHA
Mother's Name : Smt.	KIRAN JHA

DETAIL OF MARKS

Sr. No.	Subject Code	Subject Name	Marks obtained	Minimum Marks	Maximum Marks
1.	ESC-CSE-301G	MICROPROCESSOR	032	030	075T
			010 042	040	025S 100
2.	LC-CSE-323G	COMPUTER NETWORKS LAB	021	010	025P
			019 040	020	025S 050
3.	LC-CSE-325G	DESIGN & ANALYSIS OF ALGORITHMS USING C++	021	010	025P
			018 039	020	025S 050
4.	LC-CSE-327G	PROGRAMMING IN JAVA LAB	019	010	025P
			020 039	020	025S 050
5.	LC-ESC-321G	MICROPROCESSOR LAB	021	010	025P
			019 040	020	025S 050
6.	PCC-CSE-303G	COMPUTER NETWORKS	033	030	075T
			013 046	040	025S 100
7.	PCC-CSE-305G	FORMAL LANGUAGES & AUTOMATA	044	030	075T
			010 054	040	025S 100
8.	PCC-CSE-307G	DESIGN & ANALYSIS OF ALGORITHMS	042	030	075T
			015 057	040	025S 100
9.	PCC-CSE-309G	PROGRAMMING IN JAVA	035	030	075T
			014 049	040	025S 100
10	PEC-CSE-311G	SOFTWARE ENGINEERING	044	030	075T
			022 066	040	025S 100
11	PT-CSE-329G	PRACTICAL TRAINING-I	B		G
	TOTAL :		0472		0800

Result : PASSED AND HAS OBTAINED FOUR HUNDRED SEVENTY TWO MARKS
Dated : FEB 07, 2024
Place : Rohtak
Checked by 1. _____ 2. _____
(Full Signature) (Clerk) (Assistant)
Date of issue 01 APR 2024

CONTROLLER OF EXAMINATIONS

MAHARSHI DAYANAND UNIVERSITY, ROHTAK

(Established Under Haryana Act No. 25 of 1975)
("A+" Grade, NAAC Accredited)

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DMC No. 34096265
Roll No. 4054188
Regn. No. 2013761158

RESULT-cum-DETAILED MARKS CARD

BACHELOR OF TECHNOLOGY
(COMPUTER SCIENCE & ENGG.) 6TH SEMESTER

EXAM - MAY 2023

Name :	UJJAWAL KUMAR JHA
Father's Name : Shri	RAMLAKHAN JHA
Mother's Name : Smt.	KIRAN JHA

DETAIL OF MARKS

Sr. No.	Subject Code	Subject Name	Marks obtained	Minimum Marks	Maximum Marks
1.	ESC-CSE-308G	MOBILE AND WIRELESS COMMUNICATION	036 018 054	030 040	075T 025S 100
2.	LC-CSE-324G	COMPILER DESIGN LAB	019 020 039	010 020	025P 025S 050
3.	LC-CSE-326G	ARTIFICIAL INTELLIGENCE LAB USING PYTHON	017 018 035	010 020	025P 025S 050
4.	LC-CSE-328G	ADVANCED JAVA LAB	018 019 037	010 020	025P 025S 050
5.	MC-317G	CONSTITUTION OF INDIA	A		G
6.	PCC-CSE-302G	COMPILER DESIGN	042 018 060	030 040	075T 025S 100
7.	PCC-CSE-304G	ARTIFICIAL INTELLIGENCE	053 018 071	030 040	075T 025S 100
8.	PCC-CSE-306G	ADVANCED JAVA	039 016 055	030 040	075T 025S 100
9.	PEC-CSE-314G	COMPUTER GRAPHICS	035 018 053	030 040	075T 025S 100
10	PEC-CSE-320G	DATA SCIENCE	062 018 080	030 040	075T 025S 100
11	PROJ-CSE-322G	PROJECT-I	020 021 041	010 020	025P 025S 050
	TOTAL :		525		800

Result : PASSED AND HAS OBTAINED FIVE HUNDRED TWENTY FIVE MARKS
Dated : JUL 17, 2023
Place : Rohtak
Checked by 1. _____ 2. _____
(Full Signature) (Clerk) (Assistant)
Date of issue 01/09/2023

CONTROLLER OF EXAMINATIONS

MAHARSHI DAYANAND UNIVERSITY, ROHTAK

(Established Under Haryana Act No. 25 of 1975)
("A+" Grade, NAAC Accredited)

J973

DMC No. 34159605
Roll No. 5066498
Regn. No. 2013761158

RESULT-cum-DETAILED MARKS CARD

BACHELOR OF TECHNOLOGY
(COMPUTER SCIENCE & ENGG.) 7TH SEMESTER

EXAM - DEC 2023

Name :	UJJAWAL KUMAR JHA
Father's Name : Shri	RAMLAKHAN JHA
Mother's Name : Smt.	KIRAN JHA

DETAIL OF MARKS

Sr. No.	Subject Code	Subject Name	Marks obtained	Minimum Marks	Maximum Marks
1.	PCC-CSE-401G	NEURAL NETWORKS	050 020 070	030 040	075T 025S 100
2.	PEC-CSE-403G	SOFTWARE PROJECT MANAGEMENT	046 021 067	030 040	075T 025S 100
3.	PEC-CSE-411G	NETWORK SECURITY AND CRYPTOGRAPHY	061 022 083	030 040	075T 025S 100
4.	HSMC-08G	FUNDAMENTALS OF MANAGEMENT	056 021 077	030 040	075T 025S 100
5.	LC-CSE-421G	NEURAL NETWORKS LAB	021 021 042	010 020	025P 025S 050
6.	PROJ-CSE423G	PROJECT-II	045 042 087	020 040	050P 050S 100
7.	PT-CSE-425G	PRACTICAL TRAINING-II	A		G
TOTAL :			0426		0550

Result : PASSED AND HAS OBTAINED FOUR HUNDRED TWENTY SIX MARKS
Dated : FEB 07, 2024
Place : Rohtak
Checked by 1. _____ (Clerk) 2. _____ (Assistant)
(Full Signature)
Date of issue 01 APR 2024

CONTROLLER OF EXAMINATIONS

J973

MAHARSHI DAYANAND UNIVERSITY, ROHTAK

(Established Under Haryana Act No. 25 of 1975)
("A+" Grade, NAAC Accredited)



DMC No. 40357019
Roll No. 1064865
Regn. No. 2013761158

RESULT-cum-DETAILED MARKS CARD

BACHELOR OF TECHNOLOGY
(COMPUTER SCIENCE & ENGINEERING) 8TH SEMESTER

MAY 2024

Name :	UJJAWAL KUMAR JHA
Father's Name : Shri	RAMLAKHAN JHA
Mother's Name : Smt.	KIRAN JHA

DETAIL OF MARKS

Sr. No.	Subject Code	Subject Name	Marks obtained	Minimum Marks	Maximum Marks
1.	PCC-CSE-402G	MACHINE LEARNING	060	030	075T
			019 079	040	025S 100
2.	PCC-CSE-404G	BIG DATA ANALYTICS	040	030	075T
			021 061	040	025S 100
3.	LC-CSE-410G	BIG DATA ANALYTICS LAB	021	010	025P
			020 041	020	025S 050
4.	LC-CSE-412G	MACHINE LEARNING WITH PYTHON LAB	020	010	025P
			020 040	020	025S 050
5.	PROJ-CSE-422G	PROJECT-III	040	020	050P
			040 080	040	050S 100
6.	OEC-CE-448G	TRAFFIC ENGINEERING AND ROAD SAFETY	054	030	075T
			019 073	040	025S 100
		TOTAL :	0374		0500
		TOTAL : I TO VII SEM	3117		4900
		GRAND TOTAL :	3491		5400

Result : PASSED AND HAS OBTAINED THREE THOUSAND FOUR HUNDRED NINETY ONE MARKS

Dated : JUL 29, 2024

Place : Rohtak

Checked by 1. _____ (Clerk) 2. _____ (Assistant)
(Full Signature)

Date of issue 09 SEP 2024

CONTROLLER OF EXAMINATIONS